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Proc. SPIE 3508, Multilevel Interconnect Technology II

Copper interconnect technology (book, 2001)

Copper interconnect technology. [Christoph Steinbruchel; SPIE Optical Engineering Press, " Tutorial texts in optical engineering ; "

Patent us6358832 - method of forming barrier

Method of forming barrier layers for damascene interconnects "Copper Chip" Technology, SPIE Conference on Multilevel Copper interconnect structures and

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chips using this technology can have smaller metal components, Damascene processes generally form and fill a single feature with copper per Damascene stage.

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